

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		09/801,843	FUKUDA ET AL.	
		Examiner	Art Unit	Page 1 of 1
		Barry J. O'Brien	2183	

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,088,783	07-2000	Morton, Steven G	712/22
	B	US-5,387,983	02-1995	Sugiura et al.	358/434
	C	US-6,530,010	03-2003	Hung et al.	712/1
	D	US-5,463,732	10-1995	Taylor et al.	345/505
	E	US-2001/0049763	12-2001	Barry et al.	710/264
	F	US-5,274,468	12-1993	Ojha, Purnendu S.	358/448
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Short, Embedded Microprocessor Systems Design: An Introduction Using the Intel 80C188EB, 1998, Prentice-Hall, Inc., pgs.468-469.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.